



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of:

Warren M. Farnworth

Serial No.: 09/548,942

Filed: April 13, 2000

For: METHOD AND APPARATUS OF  
INTERPOSING VOLTAGE REFERENCE  
TRACES BETWEEN SIGNAL TRACES IN  
SEMICONDUCTOR DEVICES

Confirmation No.: 6934

Examiner: B. Lee

Group Art Unit: 2817

Attorney Docket No.: 2269-4161US  
(98-1265.00/US)

Notice of Allowance Mailed:

September 14, 2006

*Approved for Entry*  
*3*

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EV 826305885 US

Date of Deposit with USPS: November 29, 2006

Person making Deposit: Brett A. Hooke

AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

**Amendments to the specification** appear on page 2 of this paper;

**Amendments to the claims** are set forth in the listing of the claims that begins on page 3  
of this paper; and

**Remarks** start at page 6 of this paper.